

Run length limited error control codes construction based on one control matrix property

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In this manuscript a simple method is presented for constructing run length limited error control codes from linear binary block codes. The run length limited properties are obtained via addition of a carefully chosen fixed binary vector - a modifier to all codewords without introducing any additional redundancy. Modifier selection is based on a specific property, which can be found in some of the linear binary block codes control matrices. Similar known methods are based on properties of generator matrices. However some codes are specified via control matrices, for example low density parity check codes. The method proposed in this letter could be applied to some of them directly. This is illustrated in this manuscript using example in which a run length limited low density parity check code is obtained from Gallager code.

Key words: run length limited error control codes, control matrix, linear block codes, modifier

1 Introduction

A run-length limited error control code (RLL-ECC) has constrained run-lengths of identical symbols and can be used for error control. The research of RLL-ECC can be traced back to the late eighties of the 20-th century [1–2]. In [3] a new modified construction was presented yielding improved RLL properties for DC free error-correcting codes. In [4] trellis RLL-ECCs were constructed. In [5] a RLL-ECC for one error correction was proposed. In [6] a systematic approach was given for obtaining RLL-ECCs from binary error-correcting codes. The storage system industry motivated research into relatively complex RLL-ECCs based on turbo codes and low density parity check (LDPC) codes [7–11]. Recently, new RLL Codes were proposed for visible light communication (VLC) systems together with soft decoding methods [12–14]. In [15] algorithms were presented for code design using finite state machines which simultaneously provide a coding gain while also mitigating flicker in VLC. In [16–23] RLL-ECCs were constructed using the arrangement illustrated in Fig. 1, from linear binary block codes (LBBC) using modifiers obtained based on generator matrix properties. This method has a disadvantage, that it cannot be used directly for codes that are specified via control matrices, for example LDPC codes. In contrast to this method, the approach proposed in this letter could be applied to some of them directly because the modifiers are selected using a specific property, which can be found in some LBBC control matrices.

2 Basic properties of Linear Binary Block Codes Matrices

If C is a k -dimensional LBBC in which each codeword \mathbf{c} has length n , it could be defined using a $[k \times n]$ generator matrix over a finite field $GF(2)$. The control matrix for C is defined in [24] as

$$C = \{\mathbf{c}; \mathbf{cH}^T = \mathbf{0}\}. \quad (1)$$

Each row of \mathbf{H} describes one control equation, which each codeword must fulfil. For example the following \mathbf{H} matrix

$$\mathbf{H} = \begin{bmatrix} 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 \end{bmatrix}, \quad (2)$$

which describes a $[7, 4, 3]$ Hamming code, defines for each codeword $\mathbf{c} = (c_6, c_5, c_4, c_3, c_2, c_1, c_0)$ following 3 control equations

$$c_3 + c_2 + c_1 + c_0 = 0, \quad (3)$$

$$c_6 + c_5 + c_1 + c_0 = 0, \quad (4)$$

$$c_6 + c_4 + c_2 + c_0 = 0. \quad (5)$$

Note. Each LBBC contains one all zero codeword and therefore an infinite run of zeros could occur in an infinite sequence of codewords.

3 Description of the proposed construction method

In practice it is often necessary to avoid long runs of identical symbols in encoded sequences. The practical

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